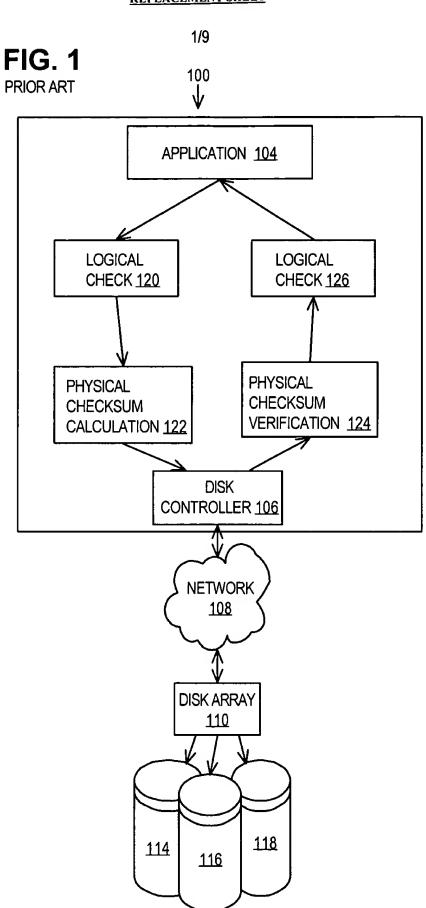
BATTER ADEM NOV 0 3 2003 Title: DATA INTEGRITY VERIFICATION MECHANISM

Inventor(s): Juan R. LOAIZA, et al. Serial No.: 09/765,680

Docket No. 50277-1633

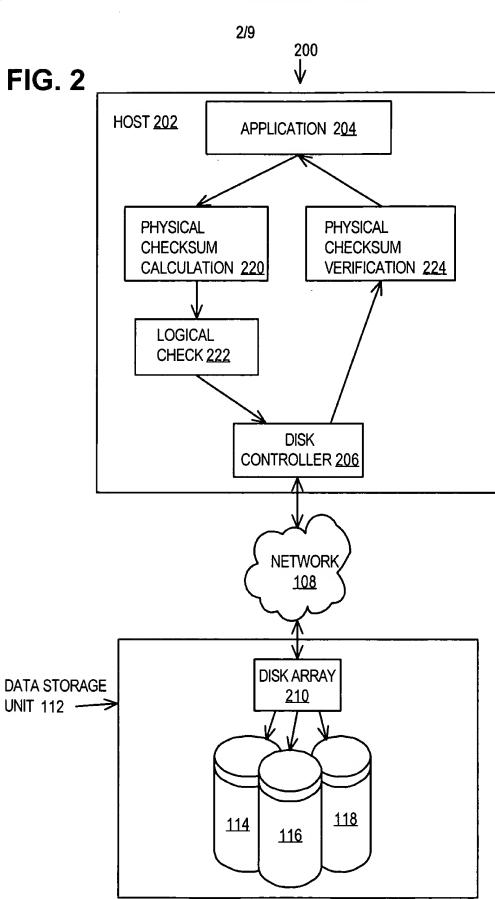




Title: DATA INTEGRITY VERIFICATION MECHANISM

Inventor(s): Juan R. LOAIZA, et al.

Serial No.: 09/765,680 Docket No. 50277-1633



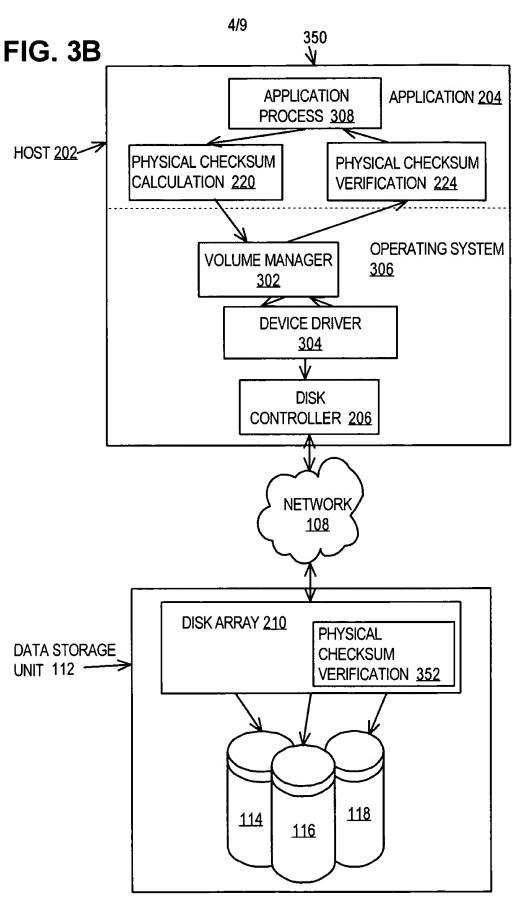
Title: DATA INTEGRITY VERIFICATION MECHANISM Inventor(s): Juan R. LOAIZA, et al. Serial No.: 09/765,680 Docket No. 50277-1633 NOV 0 3 2003 REPLACEMENT SHEET 3/9 300 FIG. 3A **APPLICATION** APPLICATION 204 PROCESS 308 HOST <u>202</u>→ PHYSICAL CHECKSUM PHYSICAL CHECKSUM CALCULATION 220 VERIFICATION 224 **LOGICAL CHECK 222** OPERATING SYSTEM **VOLUME MANAGER** 306 302 **DEVICE DRIVER** <u>304</u> DISK CONTROLLER 206 **NETWORK** <u>108</u> DISK ARRAY DATA STORAGE 210 UNIT 112 -<u>118</u> 114 116

NOV 0 3 2003

Title: DATA INTEGRITY VERIFICATION MECHANISM

Inventor(s): Juan R. LOAIZA, et al.

Serial No.: 09/765,680 Docket No. 50277-1633





Title: DATA INTEGRITY VERIFICATION MECHANISM

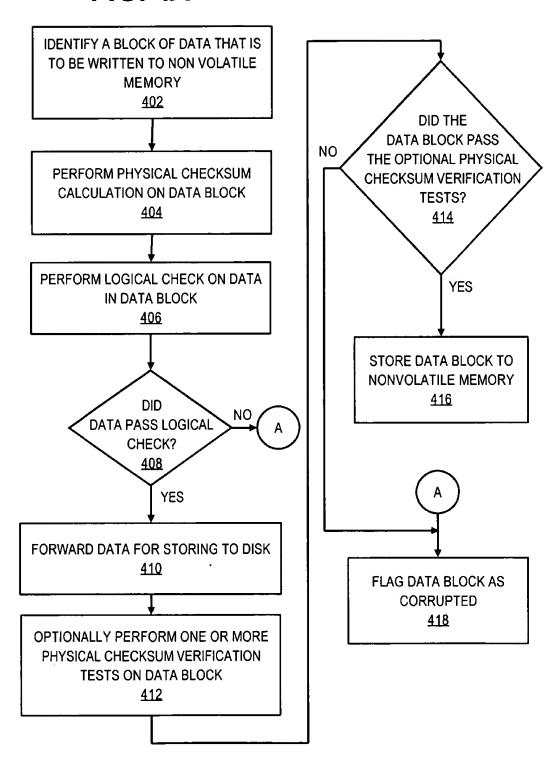
Inventor(s): Juan R. LOAIZA, et al.

Serial No.: 09/765,680 Docket No. 50277-1633

REPLACEMENT SHEET

5/9

FIG. 4A



NOV 0 3 2003

Title: DATA INTEGRITY VERIFICATION MECHANISM

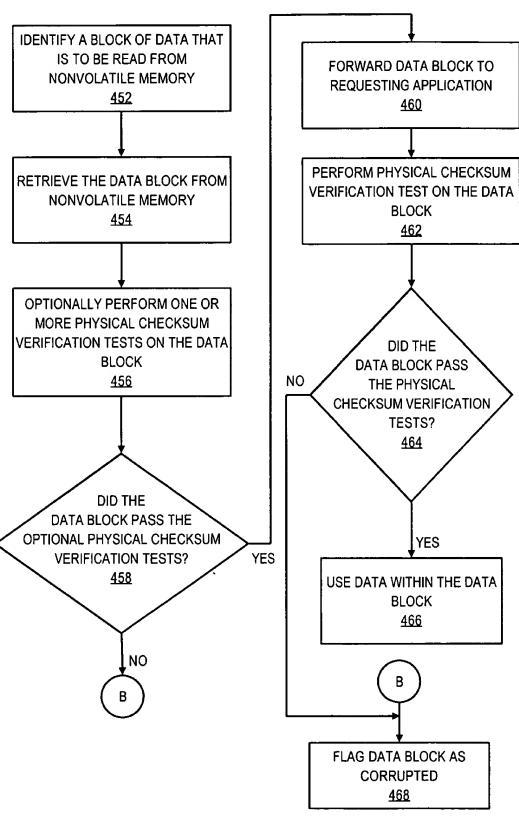
Inventor(s): Juan R. LOAIZA, et al.

Serial No.: 09/765,680 Docket No. 50277-1633

REPLACEMENT SHEET

6/9

FIG. 4B





Title: DATA INTEGRITY VERIFICATION MECHANISM Inventor(s): Juan R. LOAIZA, et al. Serial No.: 09/765,680

Docket No. 50277-1633

REPLACEMENT SHEET

7/9

FIG. 5



CHECKSUM 502 **BLOCK** LOCATION 504 DATA <u>506</u>



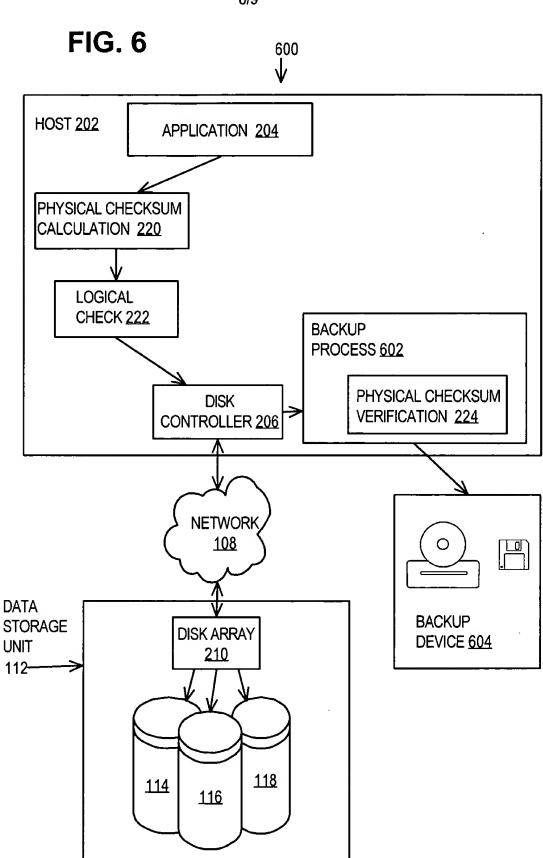
Title: DATA INTEGRITY VERIFICATION MECHANISM

Inventor(s): Juan R. LOAIZA, et al.

Serial No.: 09/765,680 Docket No. 50277-1633

REPLACEMENT SHEET

8/9



DIPE BETT A TRACEM NOV 0 3 2003 Title: DATA INTEGRITY VERIFICATION MECHANISM Inventor(s): Juan R. LOAIZA, et al. Serial No.: 09/765,680 Docket No. 50277-1633

